

QUANTUMDATA



Model 801SL Portable Video Test Generator

The 801SL is the ideal solution for repairing monitors, projectors or flat panel displays. You no longer need to sacrifice precision or programmability for portability or bench space.

The 801SL is an extremely accurate signal source that allows you to adjust to the exact factory settings – on your bench and in the field.

Over 100 standard computer formats are pre-programmed for you.

The 160 built-in test images include many manufacturers' custom images that will assure precise adjustment to the factory standards.

Main Features of the Model 801SL:

Compact Size

The 801SL will easily fit in your briefcase, leaving enough room for tools and other repair items.

160 MHz Video Pixel Rate

A programmable video clock rate up to 160 MHz allows precise convergence, focus and linearity measurements to be made on high-end computer workstation displays up to 2048 x 2048.

Many Computer Formats and Test Images Built-In

There are over 100 popular video standard formats built-in. Such as VGA, Vesa, XVGA, Sun, HP, Apple, IBM, and others. Once the correct format is selected you can select from the 160 test images included with the generator.

Easy Selection of Formats and Test Patterns

Easily select formats and images with the two front panel knobs. The names of the current format and test image are always shown on the LCD along with the H&V scan rates.

Adding Formats and Images

#1 Use the display connected to your generator as a full screen format editor (shown at right), or **#2** Use the included VGM software to control the generator via a virtual control panel operating under MS-Windows[™].

MS-Windows[™] Interface

Our Windows interface makes it easy for you to select the monitor type you're testing and the test images you need. Just point and click using a mouse.

Custom Image Editor

Create full custom images that exactly match any manufacturer's production test patterns. Our new image editor is a full-featured Windows drawing program that lets you see the image on your PC as it's being created. The images automatically scale to match different resolution displays. One hundred custom images can be saved in the generator's Flash memory, and an unlimited number on disk.

Custom Test Sequences

You can quickly set up custom sequences of formats and images for repetitive testing. Then, sequence them manually with a knob, or have a timed auto sequence for each test step. An operator will be less likely to miss a critical test step or adjustment.

Pixel Resolution Sync

You'll be sure the display is centered as precisely as it was from the factory with the pixel resolution sync accuracy.

Generator to Generator Copying

Easily and quickly copy all data from one generator to another in seconds. This is an easy way to assure each repair station has the same data.

anno datai			
Name: ABC_m02m			Pixel Rate:
Location: 173		16.257	MHz 61.512 ns
Entry Units: Ti	me		
	Horizontal		Vertical
Rate:	18.432 KHz*	49.816	Hz
Active:	720 pixels 44.289 us*	350	lines 18.989 ms*
Blank:	162 pixels 9.965 us	20	lines 1.085 ms
Period:	882 pixels 54.253 us*	370	lines 20.074 ms*
Physical size:	11.811 inches 300.000 mm	8.858	inches 225.000 mm
Pulse delay:	9 pixels 0.554 us	0	lines 0.000 ms
Pulse width:	144 pixels 8.858 us	16	lines 0.868 ms
EQ Before:		0	lines
EQ After:		0	lines
Scan :	Progressive (non-interlace)		
ACS kind:	none		On: -G-
DCS kind:	American ORed		
DSS kind:	American separate		DS Polarity: H+ V- C+
Sync select:	DSS		DS Gate: Hon Von Con
/ideo kind:	2-bit mono		Pedestal: OFF 7.5 IRE
∕ideo bias:	0.000 volts blank minus ground		
/ideo swing:	0.714 volts white minus blank		
Sync swing:	0.286 volts blank minus sync		
Gamma :	OFF 2.200		
Display code ex	pected: E Code read: F		
Exit <-Cur	sor Cursor-> Check Save	Save A	s Undo

Format Editor

Standard Connectors Built-In

Many standard video and sync connectors are built-in. There's no need for you to build or buy special adaptor cables. Just plug your display's signal cable into the matching connector on the 801SL.

Toggle RGB for Convergence

The R, G, B video and sync outputs can be individually turned on and off with separate buttons. Lit buttons indicate "on" status. This is useful for convergence adjustments.

Free Internet or BBS Access to Firmware Updates

Built-in Flash memory lets you update the 801's firmware from a computer. Just download the latest firmware file from our Website or BBS into your 801 using your PC. This unique capability gives you fast and easy access to all our latest improvements.

RS-232 & IEEE-488 Ports

Backup, create, edit, delete and rearrange formats via a computer. Also, remotely control all the front panel functions.

ATE Applications

If you want to write your own application to control the 801SL, our software will meet your needs. We've included the Source files and Drivers you'll need whether you're in a DOS or a Windows environment. Contact our applications department for more information.



801SL Specifications

Images – Ov	/er 160 B	uilt-in			
Colorbars	4	Focus	9		
CrossHatch	10	Text	4		
Dots	3	HV Reg	1		
Grill	8	Flat Field	4		
SMPTE133	1	Animated	2		
Linearity	2	Gravbar	2		
	2 110 (mor	olaybai	L in house images		
III-IIOuse	IIU (IIUI	heir menufacturers	a in-nouse inages		
	used in ti	neir manufacturing	process)		
Custom Ima	aes				
Storage:	100+ ima	ages (typical)			
Edit Methods:	801SL in	nade editor and M	S-Windows™		
East motification.	screen e	ditor			
Edit functions:	load cau	load save delete cut copy paste clear			
Luit functions.	louoring	arid onon to arid	y, paste, clear,		
		gilu, silap-lo-gilu,	snap-to-object,		
D :	VV FSIVVI	w Siw G, preview, group, un-group, exit			
Primitives:	dot, line,	triangle, rectangle	, oval, center		
	mark, ce	ntered cross, grid,	H-grill, V-grill,		
	IO-hatch	, OI-hatch, limits, s	step #, page,		
	format da	ata block, text, info	block		
Colors:	256 <80	MHz, 16 >80MHz			
Fill patterns:	31				
Fonts:	19				
	400				
Formats - 0	ver 100 E	Built-In	_		
PAL:	6	HP:	1		
RS-170:	3	NEC:	1		
Mac:	13	VESA:	30		
Sun:	10	Barco:	3		
IBM:	16	Sony:	3		
Intergraph:	2	Other:	19		
0					
Custom For					
Storage:	300+ 101	mats (typical)			
Edit methods:	801SL F	ormat Editor and N	IS-Windows m		
	screen e	ditor			
Sequences					
Storage:	2000 ste	ns			
Edit methods:	80151 5	equence editor an	Ч		
Eait motilous.	MS Wind	lowo™ corcon odito			
Deremetere	Format i		ion		
Palameters.	Fulfial, I	inage, inage vers	1011		
Auto umes:	0.1 Seco				
Name:	8 charac	ters			
Horizontal 7	imina				
Frequency:	1 0 to 13	0 KHz			
Total nivels ner l	ine [.]				
Range	144 to 40	196 (must he even)		
Stone:	2 nivel et		/		
oteps.	2 pixel si 4 pixel si	tens $> 80 \text{ MHz}$			
A etive nivele nev	+ pixer or				
Active pixels per	10 40 00	40 /1 :: 11 00			
Range:	16 to 204	48 (LIMIT: $H_{total} - 32$	pixels)		
Steps.					
0 11 ()					
Sync delay (pixe	is, front porch	1): 	110 1)		
Range (ana	log): 1 to (H	al - H active - HS delay	– HS _{width} – 1)		
Range (digit	al): 1 to (H _{tot}	_{al} – HS _{delay} – 1)			
Step:	1 pixel s	teps ≤ 80 MHz			
	2 pixel s	teps >80 MHz			
Sync pulse width	ι (pixels):				
Range (ana	log): 1 to (H _{tot}	- Hactive - HS	- HS _{width} – 1)		
Range (digit	al): 1 to (H	– HS – 1)	Wider		
Step:	1 pixel st	teps ≤ 80 MHz			
	2 pixel st	teps > 80 MHz			
D : 1 - : -					
	3				
Frequency:	2 to 160	IVIHZ			
Step:	1.465 Hz	<u>r</u>			
Jitter:	< 400 pS	S line-to-line (1 sigr	na)		
Accuracy:	25 ppm (0 ppm when calibr	ration		
	software	controlled)			
			,,		
		FF-488	CBS-232		

Vertical Timing	9						
Frequency range: 1.0 to 650 Hz							
Vertical total scan lin Range:	es per frame: 2 to 4096 (progressive)						
Steps:	5 to 4097 (interlace) 1 line (progressive)						
Vertical active access	2 lines (interlace)						
Vertical active scan lines per frame:							
Range:	1 to 1024 lines (4095) (progr	essive)					
Stone:	2 10 1024 (4034) (IIItendoe)						
Vertical sync delay (lines front porch).						
Range:	1 to (V., – V., – VS., – VS	– 1)					
Steps: 1 line							
Vertical sync width (I	Vertical sync width (lines):						
Range:	1 to $(V_{total} - V_{active} - VS_{delay} - VS_{width} - 1)$						
Steps:	1 line						
[†] Vertical actives beyond 2048 lines require a reduced horizontal active limit. Required reductions shown in table below:							
H Active Range	Max. V Active	Displayed					
(pixels)	(lines)	Colors					
16 - 1024	4095, (4094 Interlaced)	16					
16 - 1024	2048, (2047 Interlaced)	256					
1025 - 2048	2048	10					
1025 - 2046	1024	200					
Analog Video (Dutputs						
Configuration:	RGB						
Source Z:	75 ohms						
Rise/fall times:	2.5 nSec typical						
Overshoot:	< 10% (all outputs terminated)					
Video swina	714 V						
Svnc swing.	286 V						
Setup:	0 to 100 IRE						
Calibration:	Manual adjustment (0.600V -	0.720 V)					
Output protection:	Output buffers and						
	75 ohm series termination						
Digital Video C	Outputs						
Connector:	9 pin D-Sub						
Configuration:	MDA, CGA & EGA						
Signals:	digital VI (MDA); digital RGB						
	digital RGBI (CGA)						
	digital RrGgBb (EGA)						
Source Z:	75 ohms (±2%)						
Rise/fall times:	2.5 nSec maximum						
Lovele:	\leq 4.0 nSec typical 0 = 0.1/(1 = 5.1/(open circuit))						
Levels:	0 = 0 V, $1 = 5 V$ open circuit 0 = 0 V $1 = 2 5 V$ terminate	d					
_		u					
Sync	0						
Modes:	Separate Horiz. & Vert. Comp	oosite					
Composite conligura	Monicon HS OP'd & VS: Sor	rated					
	American Serr & Ed (interlace	naleu ad)					
	Furopean HS OR'd & VS' Se	rrated					
	European Serr.&Eg (interlace	ed)					
Eq. pulse width:	(HS _{width} / 2) pixels	-					
Serr. pulse width:	$H_{total} - HS_{width}$ pixels (program) ($H_{total}/2$) - HS_width pixels (inter	mable) rlaced)					
Equalization interval	wan wan '	,					
Before:	0 to (V _{total} - V _{active} - VS _{width} - Ec	actual)					
After:	0 to (V _{total} - V _{active} - VS _{width} - Ec	before)					
Steps:	1 line						
Interlace modes:							
American (interval = prog. value)							
	European (interval = prog.val	ue5 line)					

User Interface 16 x 2 character LCD Front Panel LED power indicator Format and image selector knobs Invert, video gate, sync gate, output, and on/off rocker MS-Windows: 3.1, 95, NT Virtual front panel Format, image and sequence editors File managment Configure (start-up) file editor MS-DOS Command line: Terminal emulator for interacting directly with the 801SL **Computer Interface** We supply the code necessary to connect the 801SL to Automated Test Equipment (ATE) applications. MS-Windows DLL: A DLL is supplied that lets you use the 801SL's command library in your MS-Windows application MS-DOS Driver: Use the 801SL's command library in your MS-DOS application. MS-DOS send utility: For sending command files that customize the 801SL for your application. C source code: Complete C source code is supplied that lets you use the 801SL's command library in your C application. Windows NT: Driver (optional) **Computer Ports** IEEE-488 interface Protocol: IEEE-488.2 Connector: 24 position microribbon Serial interface: RS-232 Type: 300 through 38,400 Baud rates: Data: 7,8 Stop: 1, 2 none,odd,even Parity: Handshake: none, RTS/CTS 9 pin D-Sub receptacle Connector: 801SL copy: Baud rate: 38,400 Protocol: Y-Modem Batch Other Characteristics AC Mains: Frequency: 47 - 440 Hz 90 - 250 VAC Voltage: Power: 20 watts Weight (unpacked): 5-1/2 lbs. (2.5 kg) Size (unpacked): 3-1/4 in. x 10 in. x 10-1/2 in. (8.3cm x 25.4 cm x 26.0 cm) Options Carrying handle: Locking handle also functions as a stand to support the generator on a tabletop. Mounts on or below a desk; allows Case mount: the 801SL to tilt to a maximum angle of 45° for easy viewing. Rack mounting kit: Consists of a rack panel, mounting brackets, and instructions for installing the 801SL into an instrument rack.



801SL Rear Panel diagram, showing connector and port locations (not to scale).

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