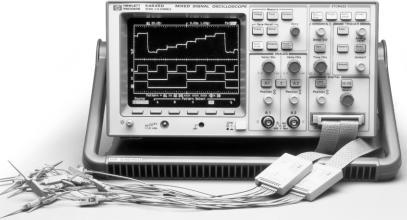


HP 54645D Mixed Signal Oscilloscope

• Dual-channel 100-MHz scope with 200 MSa/s

- 1 MB of memory per scope channel
- 16 logic timing channels with 400 MSa/s on 8 channels (2 MB memory/ch) 200 MSa/s on 16 Channels (1 MB memory/ch)
- Ideal for debugging 8- or 16-bit microcontroller systems
- MEGA(Z)OOM technology for easy-to-use responsive deed memory
- Simple easy-to-use controls
- Powerful triggering



HP MEGA(\mathbf{Z})00m Technology

When trying to view analog and digital signals, the events of interest either take place over a long time span or they are widely separated from the trigger event. With 1 million samples per channel, MegaZoom technology captures long time spans while maintaining the high sample rate, allowing you to see the fine detail needed to solve elusive problems.

Before the introduction of the HP 54645D MSO with MegaZoom technology, deep-memory oscilloscopes were considered specialized tools because of their complex operation, non-responsive control panel, and excessive display dead time. These problems have been eliminated with the development of MegaZoom

technology which uses multiple processors optimized for the task of waveform acquisition, storage and display. Now you can have a deep memory scope in your lab that is also a scope you will use every day as it is a deep-memory scope that responds instantly to your control inputs, has a high speed, low dead time display and deep memory with easy-to-use pan-and-zoom.

The HP 54645D MSO provides the triggering power you need to solve your troubleshooting problems with an easy-to-use control system. You will find it ideally suited for everyday use with its familiar scope edge triggering mode. This familiar scope mode is the one that will solve most of your problems as you can trigger on a rising or falling edge on any of the MSO's 18 input channels.

Product Overview

New mixed-signal testing power

With the introduction of the HP 54645D mixed-signal oscilloscope (MSO) to your lab, you will be able to easily view the complex relationships of your circuit's analog and digital operation. Seamless integration of scope and logic channels in eh HP 54645D MSO allow you to view both the analog circuit operation on the two 100 MHz scope channels and the logic timing displayed on the 16 logic channels. Analog and digital events are aligned in time so that you can easily relate cause and effect in difficult mixed-signal troubleshooting situations, such as those encountered in debugging 8or 16-bit microcontroller systems.

The HP 54645D gives you an oscilloscope-like operation of both the scope and logic channels. For example, simply turn the time-base knob to set the time/division for all scope and logic channels. Press Autoscale for the display of all active analog (scope) and digital (logic) channels. There is no scopelogic mode switch, just a seamless integration of logic channels into a scope.

Pattern triggering is provided in the HP 54645D MSO. This triggering mode allows you to establish a trigger pattern of high, low and don't care levels across all 18 of its channels.

The advanced mode gives you the choice of glitch, TV and advanced pattern triggering. In the glitch mode, the HP 54645D MSO can search for a glitch that is less than a specified width on any of its 18 input channels, allowing you to find abnormally short pulses that indicate circuit failures. In addition you can search for a pulse that is greater than a specified width or within upper and lower limits.

In advanced pattern trigger mode the HP 54645DMSO will search for a combination of two trigger pattern terms. These terms may be

Specifications

Vertical System	
Scope Channels:	CH 1 and 2
Bandwidth (3dB)	dc to 100 MHz @ ≥ 10 mv/div (> 75 MHz @ < 10 mv/div)
ac coupled	1.5 Hz to 100 MHz
Risetime (calculated)	≈ 3.5 ns @ > 10 mv/div, (< 4.6 ns @ < 10 mv/div)
Dynamic Input Range	\pm 32 V or \pm 8 div whichever is less
Math Functions	channel 1 + or - channel 2
Input Resistance	1 Mohm
Input Capacitance	≈ 13 pf
Maximum Input	400V (dc + peak ac)
Range	1mV/div to 5V/div
Vertical Gain Accuracy	± 1.5% full scale
Vernier	Fully calibrated, accuracy ± 3% full scale
Single Cursor Accuracy	Vertical gain accuracy \pm 1% full scale \pm 0.5% position value
Dual Cursor Accuracy	Vertical gain ± 0.8% full scale
BW Limit	Approx. 20 MHz
Coupling	ac, dc, GND
Channel Isolation	dc to 20 MHz > 40 dB (with channels at same v/div) 20 MHz to 100 MHz > 30 dB
Inversion	Channel 1 and Channel 2

combined in one of several Boolean relationships (AND, OR, THEN).

In TV mode the HP 54645D MSO may be triggered on field 1, field 2, or line of a composite TV waveform.

Computer and hardcopy I/O

For connection to your PC, printer, or workstation, the HP 54645D is fully compatible with the full line of HP 546XX interface modules. Select the module that best fits your needs and you are ready to either print the screen or interface to your PC or workstation. With the addition of the HP 54657A or HP 54659B Measurement/Storage module you will have both the interface capabilities described above, as well as additional measurements such as FFT.

Logic Channels	
16 channels (0-15) in two pods of 8 channels each	
Maximum Input Voltage	± 40 volts peak
Threshold Range	± 6.0 volts in 50 mV increments
Threshold Accuracy	± (100 mV + 3% of threshold setting)
Input Dynamic Range	± 10 Volts about threshold
Minimum Input Voltage Overdrive	To meet the timing specifications the threshold value must be within 20% of the 50% voltage point of the input signal
Minimum Input Voltage Swing	500 mV peak to peak
Input Resistance	100 K Ω
Input Capacitance	Approx 8 pF
Channel-to-Channel Skew	2 ns typical, 3 ns max
Pre-defined Thresholds	TTL = 1.4V, CMOS = 2.5V, ELC = -1.3V
Horizontal System, Scope & Logic Channels	

Sweep Speeds	50s/div to 5 ns/div main and delayed
Accuracy	± 0.01%
Vernier	Accuracy = $\pm 0.05\%$
Horizontal Resolution	40 ps

Software for enhanced connectivity

With the addition of HP BenchLink Scope software for Microsoft[®] WindowsTM, you have the ability to easily interface this powerful instrument to your PC. This versatile software, which is compatible with Windows 3.1, 95 or NT, makes the movement of waveform data or trace images fast and easy.

Built to last

The HP 54645D MSO is designed and built to the rugged requirements of MIL-T-28800. This means that the product is built to withstand the rigors of daily use as you test and debug your circuits, backed up with a three-year warranty.

Scope Cursor Accuracy	
Single Channel	Horizontal accuracy ± 0.2% screen width ± 40 ps
Channel to Channel	Horizontal accuracy ± 0.2% screen width ± 80 ps
Logic Cursor Accuracy	
Single Channel	Horizontal accuracy ± 0.2% screen width ± 1 logic sample period
Channel to Channel	Horizontal accuracy ± 2% screen width ± 1 logic sample period ± chan-to- chan skew < 10 ppm
Delay Jitter	< 10 ppm

Delay Range

Pre-trigger (negative delay): At least 1 screen width or 2.5 msec

Post trigger (from trigger point to end of sweep): 500 seconds

Delayed Sweep

Delayed timebase can be as fast as 5 nsec/div but must be at least 2X the main timebase. Delayed sweep display is the same data acquisition as was the main.

HP MegaZoom technology (Post acquisition Pan and Zoom): The time/div and delay controls allow any part of the acquired waveform display to be expanded to the full extent of the memory available.

Modes	Auto, Autolevel, Normal,
Holdoff	and Single
	\approx 200 ns to \approx 25 seconds
Edge Triggering	Rising or falling on any of the 18 input channels
Pattern Triggering	A pattern of high, low, and don't care levels and a rising or falling edge can be established across all 18 channels. The analog channel's high level is defined by that channel's trigger level.
Advanced Triggering	Selectable as glitch, advanced pattern, or TV
Glitch	Less than, greater than, or within specified range
Source	Any of the 18 input channels
Polarity	Rising or falling
Minimum Pulse Width Setting	8 ns
Advanced Pattern	Up to two trigger terms (P1 and P2) and two edge terms (E1 and E2) may be established and these term can be combined as follows: AND, OR, Then, Entered, Exited, Duration <, Duration >, Duration range:
TV	Available on scope channels only
TV Line and Field	0.5 divisions of composite sync required for stable display
Oscilloscope Ana	log Triggering
Sensitivity	DC to 25 MHz > 10 mV/div \leq 3.5 div or 3.5 mV $<$ 10 mV/div \leq 1 div or 2 mV
	25 MHz to 100 MHz > 10 mV/div ≤ 1 div or 10 mV
	< 10 mV/div, \leq 1.5 div or 3 mV
Sources CH 1, CH 2, an	d line
Coupling	
Counting	

dc, ac, HF reject, LF reject, noise reject HF reject and LF reject -3dB @ 50 kHz.

XY

Bandwidth	100 MHz	
Phase error @ 1 MHz	1.8 degrees	

Maximum Display Rate	3 million samples per
	second with sufficient
	trigger rate, and vectors off
	60 full screens per second, vectors on
Average	Selectable as smoothing,
	4, 8, 16, 32, 64, 128, and 256 averages
Roll Mode	At sweep speeds of
	200 ms/div and slower, data moves across the
	display from right to left with no dead time
Oscilloscope Acqu	isition System
Maximum Sampling Rate	200 MSa/s on each channe
Single Shot Bandwidth	50 MHz
Simultaneous capture on	
Vertical Resolution	8 bits
Peak Detection	Can capture and display a pulse at least 5 nsec wide
	at any timebase setting
Maximum Memory Depth	at any timebase setting 1 MB samples per channel
	1 MB samples per channel
Logic Acquisition S	1 MB samples per channel System
Logic Acquisition S	1 MB samples per channel System 1 bit
Logic Acquisition S Vertical Resolution Maximum Sampling Rate	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels
Logic Acquisition S Vertical Resolution Maximum Sampling Rate	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on Peak Detection	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a pulse at least 5 nsec wide at any timebase setting 2 MB samples per channel
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on Peak Detection	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a pulse at least 5 nsec wide at any timebase setting 2 MB samples per channel on one pod, 1 MB samples
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on Peak Detection	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a pulse at least 5 nsec wide at any timebase setting 2 MB samples per channel
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on Peak Detection	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a pulse at least 5 nsec wide at any timebase setting 2 MB samples per channel on one pod, 1 MB samples
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Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on Peak Detection Maximum Memory Depth Display System	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a pulse at least 5 nsec wide at any timebase setting 2 MB samples per channel on one pod, 1 MB samples when both pods are used 7-inch raster monochrome
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on Peak Detection Maximum Memory Depth Display System Display	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a pulse at least 5 nsec wide at any timebase setting 2 MB samples per channel on one pod, 1 MB samples when both pods are used 7-inch raster monochrome CRT 255 vertical by 500 horizontal points Front-panel intensity
Logic Acquisition S Vertical Resolution Maximum Sampling Rate Simultaneous capture on Peak Detection Maximum Memory Depth Display System Display Resolution	1 MB samples per channel System 1 bit 400 MSa/s on one pod, 200 MSa/s on two pods all channels Will capture and display a pulse at least 5 nsec wide at any timebase setting 2 MB samples per channel on one pod, 1 MB samples when both pods are used 7-inch raster monochrome CRT 255 vertical by 500 horizontal points

Auvaliceu realures	
Automatic Measurements	(Measurements are continuously updated, markers indicate measurement)
Voltage	V_{AVG} (dc), $V_{\text{RMS}},$ $V_{\text{PP}},$ V_{MIN} $V_{\text{MAX}},$ $V_{\text{TOP}},$ and V_{BASE}
Time	Frequency, period, + pulse width, - pulse width duty cycle, rise time and fall time (rise time and fall time are scope only)

Cursors	Manually or automatically placed read out of time, 1/time, voltage. Additionally logic channels can be displayed as binary or hex values.
Setup Functions	
Autoscale	Finds and displays all active scope and logic channels, sets edge trigger mode on highest numbered channel, sets vertical sensitivity on scope channels and thresholds on logic channels, time base to display 1.8 periods
Save/Recall (non-volatile)	10 set-ups can be saved and recalled from non- volatile memory
Trace Trace (pixel) Memory	2 volatile
User-Defined Channel Labels	All channels may be assigned a user-defined label of up to 6 characters. Labels displayed in place of 1st division of wave form
General	
Calibrator Output: Frequency	≈ 1.2 kHz
Amplitude	5V
EMI	
Commercial	Meets FTZ 1046 class B
Mil-T-28800D	Meets requirements in accordance with paragraph 3.8.3 EMI Type III, and MIL-STD-461C as modified by table XII.
CE01, CE03	Yes
CS01, CS02, CS06	Yes
RE01	15 dB relaxation to 20 kHz; exceptioned from 20 kHz to 50 kHz
RE02 (with Opt 002)	Full limits of class A1c and A1f
RE02 (without OPT 002)	10 dB relaxation from
	14 kHz to 100 kHz
RS02	14 kHz to 100 kHz Exceptioned



General Information

Size	35.258 x 17.272 x 31.75 cm 12.7W x 6.8H x 12.5D in (excluding handle)
Weight	≈ 6.35 kgs (14 lbs)
Power Usage	≈ 90 W
Voltage	88-250 VAC
Line Voltage selection	Automatic
Frequency	45-440 Hz

Environmental Characteristics

This instrument meets the requirements of MIL-T-28800D for Type III, Class 3 Style D equipment as described below.

Shock: HP class B1 and MIL-T-28800 style D, Class 3 operating: 30g, 1/2 sine, 11 ms duration, 3 shocks per axis along major axis. Total of 18 shocks.

Vibration Operations: 15 minutes along each of 3 major axes; 0.64 mm (0.025 inch) p-p displacement, 10 Hz to 55 Hz in one-minute cycles. Held for 10 minutes at 55 Hz (4 g at 55 Hz).

Altitude: Operating to 4500 M (15,000 ft), non-operating to 15,000 M (50,000 ft).

Humidity: Operating 95% RH at 40 °C, 24 hrs, Non-operating 90% RH at 65 °C, 24 hrs Ambient temperature: Operating -10 °C to 55 °C, non-operating -51 °C to +71 °C

Safety: CSA Certification, IEC 348

Ordering Information

HP 54645D Mixed Signal Oscilloscope

Accessories included

Two each HP 10074A 10:1 divider probes with readout; HP 10089A 16 channel logic input probe assembly; Removable front panel ground connector; User's Guide and service manual; power cord.

Accessories and Options Available

Opt. 001 RS-02 magnetic interference shielding added to the CRT Opt. 002 RE-02 Display shield added to the CRT to reduce radiated interference OPT 101 HP 10098A Front panel cover and pouch kit OPT 103 HP 54645A Customer training kit OPT 104 HP 1185A Carrying case OPT 106 HP 34810B HP BenchLink Scope software OPT 1CM HP 5062-7345 Rack mount kit OPT W50 Additional two years of warranty HP 10074A 10X probe with readout HP 10070A 1X probe HP 10085A HP 16:16 logic cable and termination 10089A HP 16:2 x 8 logic input probe assembly

Modules Fully Supported

- * 54650A, HP-IB I/O
- * 54652B RS-232 and Centronics I/O
- * 54657A HP-IB Measurement/storage
- * 54659B RS-232 and Centronics Measurement/storage
- * E2657A HP-IB Connectivity kit
- * E2658A RS-232 Connectivity kit

* includes measurement/storage module, BenchLink Scope and cable.

For more information about HP's nanoVolt meters and all other Hewlett-Packard basic instruments, and for a current sales office listing, visit our web site at

http://www.hp.com/go/bi.

You can also contact one of the following centers and ask for a test and measurement sales representative.

United States: Hewlett-Packard Company Test and Measurement Call Center P.O. Box 4026 Englewood, Colorado 80155-4026 1 800 452 4844

Canada: Hewlett-Packard Canada Ltd. 5150 Spectrum Way Mississauga, Ontario L4W 5G1 (905) 206 4725

Europe: Hewlett-Packard European Marketing Centre P.O. Box 999 1180 AZ Amstelveen The Netherlands (31 20) 547 9900

Japan: Hewlett-Packard Japan Ltd. Measurement Assistance Center 9-1, Takakura-Cho, Hachioji-Shi, Tokyo 192, Japan Tel: (81) 426 56 7832 Fax: (81) 426 56 7840

Latin America: Hewlett-Packard Latin American Region Headquarters 5200 Blue Lagoon Drive 9th Floor Miami, Florida 33126 U.S.A. Tel: (305) 267-4245 (305) 267-4220 Fax: (305) 267-4288

Australia/New Zealand: Hewlett-Packard Australia Ltd. 31-41 Joseph Street Blackburn, Victoria 3130 Australia 1 800 629 485

Asia Pacific: Hewlett-Packard Asia Pacific Ltd. 17-21/F Shell Tower, Times Square, 1 Matheson Street, Causeway Bay, Hong Kong Tel: (852) 2599 7777 Fax: (852) 2506 9285

Within Budget. Without Compromise.

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