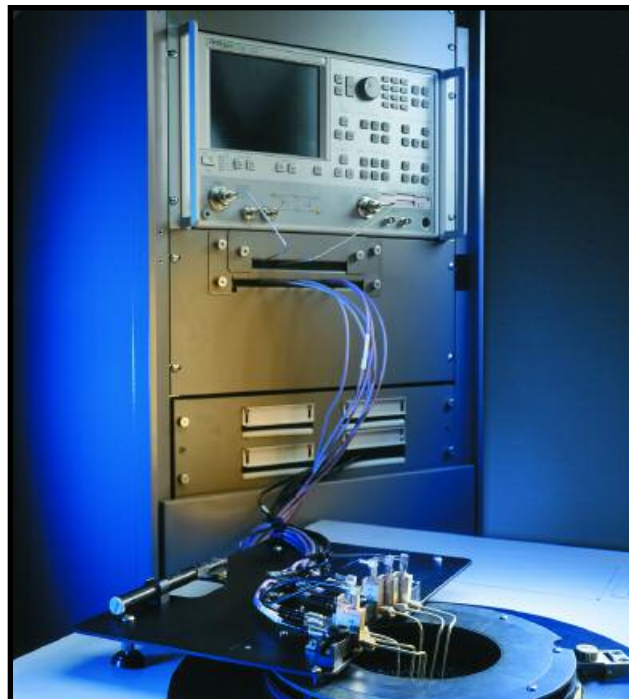
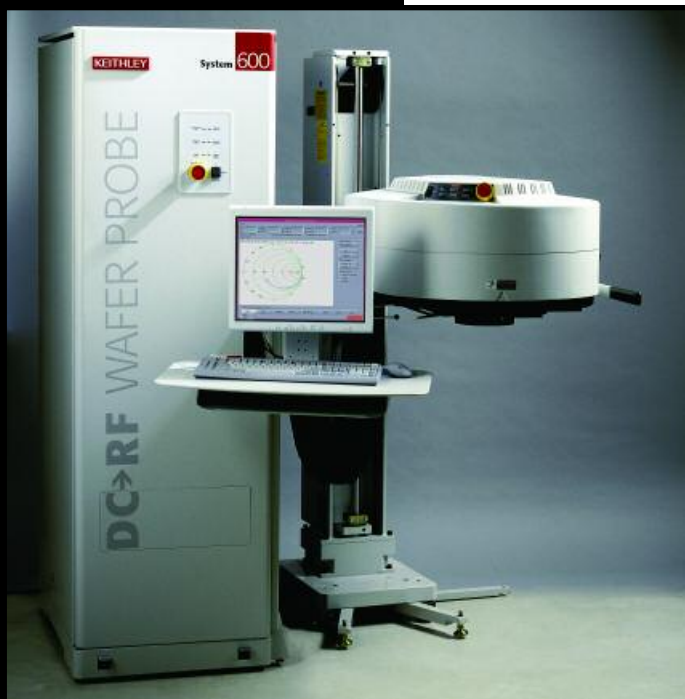


Series S400 Series S600

Parametric Test Systems



- On-wafer DC to RF testing on a single platform
- Designed to minimize Cost of Test
- Wide range of applications covered
- SECS/GEM automation for 300mm and 200mm

High Capital Equipment Reuse Minimizes Cost of Test

Keithley's Series S400 and S600 parametric testers are designed to help fabs and wafer foundries reduce their Cost of Test (COT). Ongoing changes in materials, design rules, and wafer size typically produce a semiconductor process "inflection" roughly every 18 months. These rapid changes mean one of the most significant contributors to a semiconductor manufacturer's COT is capital equipment reuse, rather than the initial equipment cost. That makes it critical to be able to upgrade a parametric test system cost-effectively to address new requirements. While most fabs typically expect 70-80% capital equipment reuse across multiple process generations, Keithley parametric testers provide industry-leading capital equipment reuse over more than five technology inflections.

Series S600 Systems

The S680 tester is Keithley's latest, most powerful system configuration based on our Series S600 UNIX platform. Like our earlier Series S600 systems, it offers fabs high throughput combined with superior DC measurement capability and broad testing flexibility. The S680 has been enhanced to address emerging measurement needs, with the capability to characterize ultra-thin gate dielectrics, SOI, new devices, new materials like copper and low- κ dielectrics, femtoamp-level DC leakage, and RF measurements of high performance BiCMOS and analog devices.

Adaptable to Changing Device Technologies

Series S600 testers are well suited for use in a wide range of testing environments, including process control, process and equipment tuning and optimization, equipment qualification, Wafer Acceptance Testing, and device modeling and characterization. Their high throughput and low-level current sensitivity make them ideal for applications such as production test for new device technologies, such as System on a Chip (SOC), FeRAM, MRAM, PCRAM, and high density devices.

With the industry moving toward larger wafer sizes, fabs are being challenged to find the time to do the sampling they need without delaying production. High speed Source-Measure Units (SMUs) and high throughput software options like true parallel testing and site-level/real-time adaptive testing allow S680 users to test 300mm wafers in the same time once required to test a 200mm wafer.

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Series S400 Series S600

Ordering Information

System Configurations

Call factory to discuss configuration options for both Series S400 and S600 UNIX-based Parametric Test Systems.

Extended warranty, service, and calibration contracts are available.

Parametric Test Systems

Per-Pin Electronics Boost Low Level Accuracy

The design of Series S600 testers makes it possible to present instrumentation for current/voltage source/measurement and capacitance measurement on each DUT pin. Each pin can also provide a quiet analog ground connection or a nearly perfect open circuit, minimizing second-order effects such as leakage and parasitic capacitance, also eliminating noise artifacts.

The S680 uses active electronics on each tester pin inside a test head that mounts on top of the prober. Each probing pin has its own amplifier for bi-directional scaling of currents. The amplifiers, which are located within centimeters of the wafer, boost the low-level analog signals far above the noise "floor" of the system. The amplified signals then pass through a high speed, solid-state switch matrix on their way to the measurement instruments.

S680 Enhancements and Options

The S680 incorporates a number of important enhancements to the basic Series S600 design. Keithley's commitment to providing the highest equipment reuse in the parametric test industry ensures that almost all of these capabilities are available as field-installable upgrades to existing Series S600 systems already on the fab floor, not just in new systems.

The S680's new cabinet design provides greater instrumentation capacity for future system expansion and an integrated Sun/Solaris system controller and controller input/output. As factory automation reaches into more and more areas of the fab, the S680's SEMI-compliant, single-wire SECS/GEM capability simplifies integrating the system into the fab's environment. The SMU has also been redesigned to combine low current, high power sourcing and measurement with faster digital communication with other system components. Cost-effective options for higher throughput include true parallel testing of up to eight DUTs within a single probe touchdown in a sub-site, as well as site-level, real-time, adaptive testing capabilities.

Other instrumentation options for the S680 include:

- Additional ultra low current, high-power SMUs (up to a system total of eight)
- Additional remote testhead pre-amplifiers (up to a system total of 16 to enable 64-pin testing)
- Fast 100kHz C-V using remote testhead pre-amplifier
- Single- and dual-channel pulse generators for embedded memory measurements
- Multi-frequency LCR for dielectric layer measurements
- Frequency counter for ring oscillator measurements
- Spectrum analyzer for ring oscillator measurements
- Interface packages for a wide variety of prober models

Together, these base system enhancements and powerful options ensure the S680 provides fast test throughput, easy programming, and high measurement accuracy and precision.

RF Option for Series S600 and S400UX Systems

The RF option is designed to speed and simplify measuring RF parameters in sequence with DC parametric testing. This option, which can be specified when ordering new S680 testers or as a field-installable upgrade to existing Series S600 or S400UX systems, allows performing 2-port s-parameter testing at bandwidths up to 40GHz. It's ideal for single-insertion DC and RF testing of analog and BiCMOS ICs, such as those used in mobile wireless consumer products. Keithley developed the capabilities provided in this option by forming a working partnership with Anritsu Corporation, a leading producer of Vector Network Analyzers (VNAs).

RF Testing Applications

Device modeling is one of the most important applications for the RF option. The data produced is compatible with a number of popular device modeling packages, including BSIMPro, IC-CAP, and UTMOST. It allows device modeling labs to do simulation model fitting in a fraction of the time required for older rack-and-stack RF measurement configurations. The RF data browser included in

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High speed, UNIX-based test platforms

SEMICONDUCTOR TEST

Series S400 Series S600

Parametric Test Systems

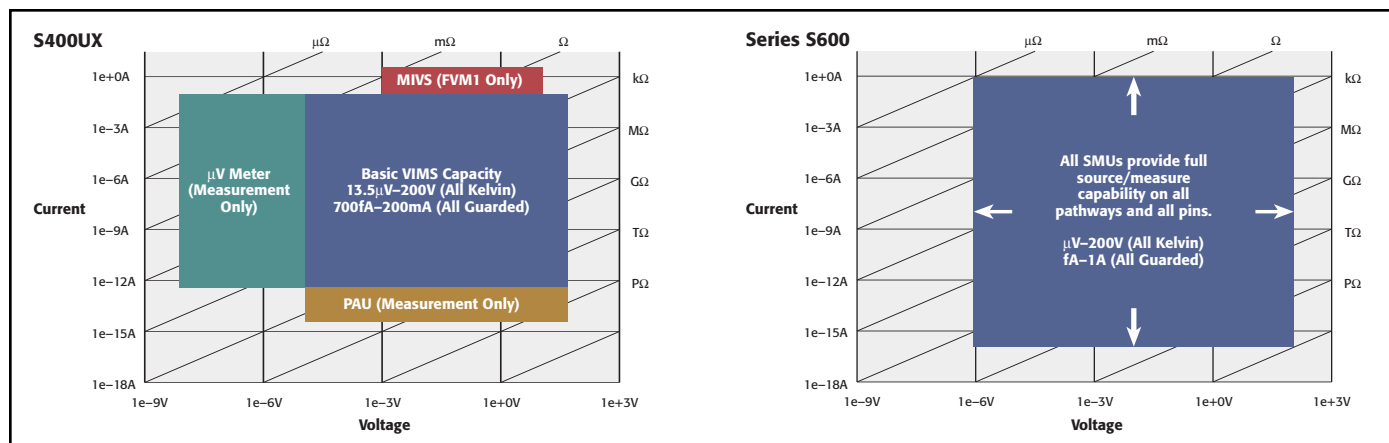


Figure 1. S400UX and Series S600 measurement ranges

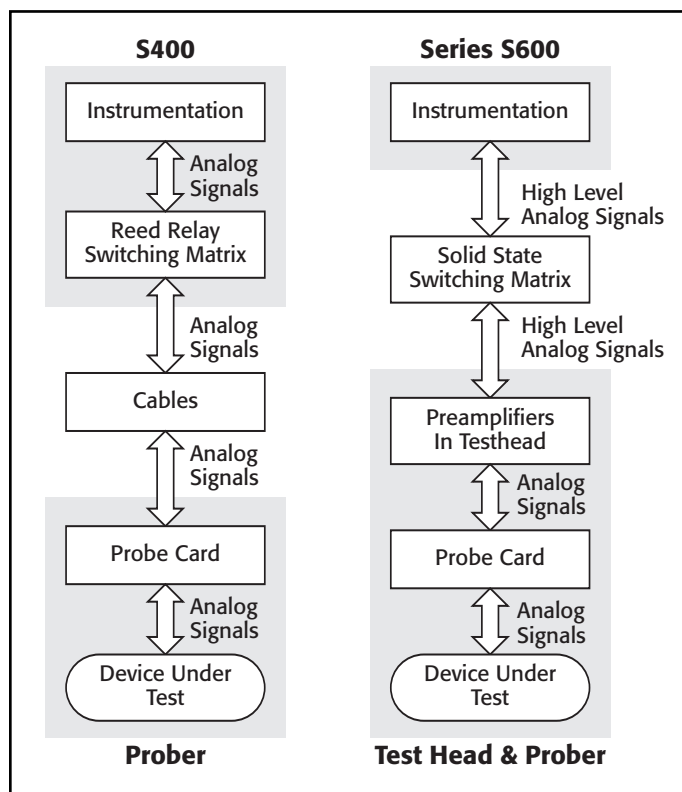


Figure 2. Comparison of the "per-pin electronics" architecture of Series S600 systems to the "cabled-out" design of S400UX systems. The Series S600 architecture improves current sensitivity by minimizing the parasitic capacitances and leakage currents inherent in systems with long cables and a reed-relay-based switch matrix between the DUT and the measurement circuitry.

the option's software reduces the time needed for data analysis from days to minutes.

Process monitoring is another common application for systems equipped with the RF option. The primary application is RFCV on sub-13Å advanced transistor gates. Test macros for converting s-parameters to RF parameters such as f_{max} , f_t , R_{bb} , and Q are also included. The system is equally well suited for general analysis of complex signals, including user-defined combinations of a_1 , a_2 , b_1 , and b_2 .

DC/RF Test Architecture

The RF option combines DC and RF testing in a single-insertion system that's practical for use in both technology development and process monitoring environments. By adding a VNA for on-wafer RF s-parameter testing at up to 40GHz to the standard DC parametric tester, the option combines two critical test functions in a single, high speed test system, increasing overall throughput dramatically. This option eliminates the long calibration and test times associated with using traditional rack-and-stack RF test solutions. It also eliminates the need for an expert to analyze and filter the data before the fab's general engineering and manufacturing community can use it.

Every Series S600 tester manufactured since 1996 can be cost-effectively upgraded with the RF option, so it's economical to incorporate RF s-parameter measurement capabilities into an existing DC tester in either a technology development or process monitoring environment. UNIX-based Series S400 testers can also be upgraded with the RF option for the technology development testing needs of a fab's modeling and process integration engineering departments.

Fabs can continue to use their existing production probers with this option, so there's no need to invest in a separate, expensive RF prober. There's no need to allocate additional expensive "real estate" on the fab floor for a separate RF test system because the combined DC/RF system occupies the same floor space as a regular DC tester does.

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Parametric Test Systems



By adding RF capabilities to an existing DC tester, the RF option also minimizes the amount of additional training needed for engineers and operators – if they already know how to run a Keithley DC tester, learning how to use the DC/RF system is much faster and simpler than starting over with a totally new system.

The long-term reliability of S400UX testers has earned them the reputation of the workhorses of the parametric test world. Although the “cabled-out” architecture of S400UX systems can’t compete with a Series S600 tester’s “per-pin electronics” design in applications that demand ultra-low-level current sensitivity, S400UX testers continue to provide economical production test solutions for a variety of high volume, low cost ICs. With the addition of RF instrumentation, they’re also a good fit for the technology development testing needs of a fab’s modeling and process integration engineering departments.

KTE provides a powerful test development and execution environment. It is compatible with Series S600 and S400UX testers, as well as the RF options for either tester. KTE's high platform-to-platform compatibility not only shortens the user learning curve when working with multiple systems, but offers a smooth migration path to protect the fab's test software investment when upgrading to higher performance systems.

The Graphical User Interface (GUI) in KTE guides test engineers through the development of a test plan. The user can create individual electrical tests at the subsite level by drawing on libraries of tests, then defining parameters and connections. To provide the ultimate in electrical test flexibility, KTE also lets users create User Libraries (custom parametric tests) using standard C. A C code generation utility simplifies this process by hiding most of the C code overhead and details.

KTE allows flexible sampling plans across individual wafers and lots. Key production control monitor tests can be performed at many wafer locations, while time-consuming tests can be assigned to a smaller set of locations. The tests to be run at each site are incorporated into a set of nested loops defined by the Keithley Test Execution Engine (KTXE). When KTXE is

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Series S400 Series S600

Parametric Test Systems

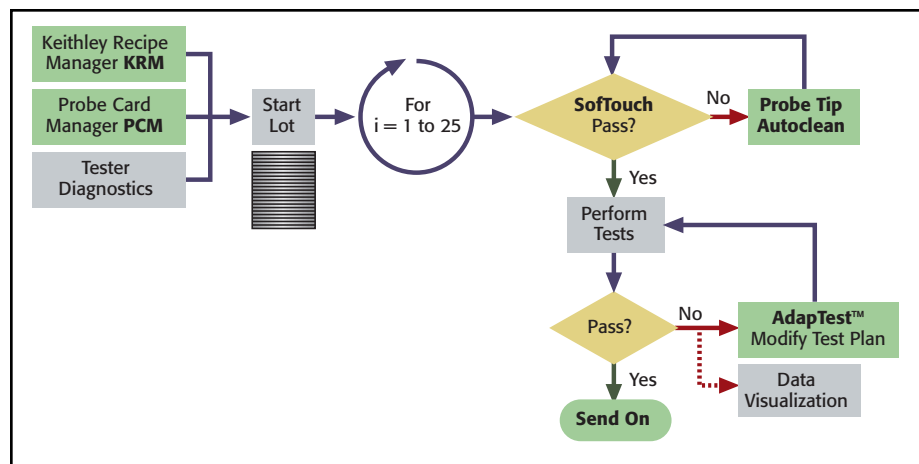


Figure 4. Keithley can provide a complete operational model for a parametric test cell.

instructed to test a lot, it processes each wafer test plan in the cassette test plan. Within these loops, User Access Points (UAPs) simplify the customization of the test execution engine without the need for re-compiling or linking. UAPs can be used to perform tasks such as transferring data to other computers when a wafer's testing is completed.

In contrast to other parametric test environments, KTE offers fabs the ability to pick and choose freely from a growing line of available software options to provide a complete operational model for the parametric test floor (see **Figure 4**). Buying one option doesn't require purchasing any others, so fabs are free to configure their software environment as their applications require. Current software options include:

- **Keithley Recipe Manager Option** (Series S600 and S400UX)—This production-worthy package lets test program developers generate valid test plans quickly by allowing them to modify existing test plans easily for reuse. Version control tools prevent unintentional, undesired, or undocumented code changes, so only approved test sequences are released to production with automatic fanout and ISO-9001 traceability for recipes.
- **Probe Card Manager Option** (Series S600 only)—The Probe Card Manager (PCM) hardware/software package automates the management of probe card inventories with on-board memory on each probe card. The PCM option can be specified for new cards and testers or retrofit on existing ones.
- **AdapTest™ Software Option** (Series S600 and S400UX)—In Keithley parametric test systems, the fundamental unit of test is a device, not a cassette. The AdapTest software enhances test throughput, product yields, process control, and automated first-level process diagnostics by allowing the tester to change test sequences very quickly and flexibly. AdapTest supports both results-based adaptive testing and preset zone-based testing. AdapTest also includes tools for electrical verification of probe-to-pad contact.
- **Parallel Test Option** (Series S600 only)—With a total of eight SMUs installed, the Parallel Test option supports true parallel testing of up to eight DUTs within a single probe touchdown in a subsite.
- **Copper Analysis Library Option** (Series S600 only)—This option provides a means of obtaining valuable information about a copper process readily. The software includes nine new copper test algorithms that can be easily incorporated into parametric test sequences. The documentation provides recommendations for copper test structures.
- **SECS/GEM Automation Option** (Series S600—300mm and 200mm) (S400UX—200mm)—This option simplifies integrating the tester into the fab's overall automation environment.

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